AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 26, lin 16 with the following paragraph:

In one example, the signal SINGLE_ERROR may be implemented as a single bit digital signal. In another example, the signal SINGLE_ERROR may be implemented as a t-bit digital signal, where t is an integer. In one example, the integer t may be equal to eight (e.g., ~~SINGLE_ERROR[0:7]~~SINGLE_ERROR[7:0]). However, the signal SINGLE_ERROR may be implemented having other bit widths accordingly to meet the design criteria of a particular application. In one example, the circuit 100 may present a single bit (e.g., SINGLE_ERROR[7]) of the signal SINGLE_ERROR. However, other bits or bit combinations of the signal SINGLE_ERROR may be presented to meet the design criteria of a particular application. The signal SINGLE_ERROR may be used to indicate that a single bit error was detected and corrected by the circuit 100 in the signals CORRECT_DATA and/or CORRECT_PARITY.

Please replace the paragraph beginning on page 29, line 16 with the following paragraph:

The circuit 128 may have one or more inputs 170a-170n that may receive the signal ERR_LOC, one or more inputs 172a-~~172(t-1)~~172(t) that may receive the signal SINGLE_ERROR, one or

2

more inputs 174a-174k that may receive the signal DATA, one or more inputs 176a-176r that may receive the signal PARITY, one or more outputs that may present the signal CORRECT_DATA, and one or more outputs that may present the signal CORRECT_PARITY. The circuit 128 may be configured to present the signals CORRECT_DATA and CORRECT_PARITY in response to the signals DATA, PARITY, ERR_LOC and SINGLE_ERROR.